

The diagram illustrates the system architecture. It consists of two main components: a **HOST DEVICE** (labeled 100) and a **MEMORY CARD** (labeled 120). They are connected via two lines: an **Access Command** line (labeled 110) and a **Data** line. The Access Command line is a single-headed arrow pointing from the Host Device to the Memory Card. The Data line is a double-headed arrow indicating bidirectional communication between the Host Device and the Memory Card.

FIGURE 2

Diagram illustrating a memory card system (120) enclosed in a dashed box. The system includes a MEMORY CARD CONTROLLER (130) and MEMORY CARD STORAGE MEMORY (140). The MEMORY CARD CONTROLLER (130) is connected to an external system (indicated by arrows on the left) and the MEMORY CARD STORAGE MEMORY (140). The MEMORY CARD STORAGE MEMORY (140) is connected to the MEMORY CARD CONTROLLER (130) and an external system (indicated by an arrow labeled "Data Retrieval Commands" on the right). Bidirectional data flow is indicated between the controller and storage memory, and between the controller and the external system on the left. A unidirectional flow of "Data Retrieval Commands" is shown from the storage memory to the external system on the right.

FIGURE 3

The diagram illustrates a system architecture enclosed in a dashed rectangular boundary. On the left is a block labeled 'Host Interface' with reference numeral 230. On the right is a block labeled 'Storage Interface' with reference numeral 240. In the center, there is a 'Memory' block (210) and a 'Processor' block (220). Bidirectional arrows connect the Host Interface (230) to the Memory (210) and the Processor (220). Bidirectional arrows also connect the Storage Interface (240) to the Memory (210) and the Processor (220). A bidirectional arrow connects the Memory (210) and the Processor (220). External data flow is indicated by arrows: 'Data (to/from host device)' enters and exits the Host Interface (230); 'Data (to/from Storage Memory)' enters and exits the Storage Interface (240). 'Access Command 110' is shown entering the Host Interface (230) from the bottom left. 'Data Retrieval Commands 130' are shown entering the Storage Interface (240) from the bottom right. Labels 'Access Command Control' and 'Data Retrieval Command Control' point to the interfaces between the Host/Storage interfaces and the Processor.

FIGURE 4

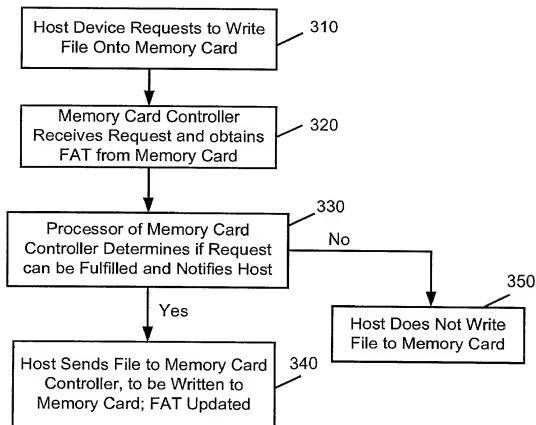


FIGURE 5

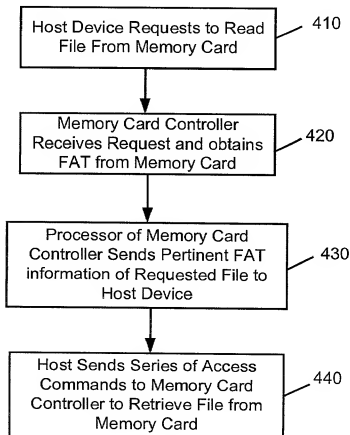


FIGURE 6

FAT
500

0000	0000
0001	FFF7
0002	0000
. . .	
000F	0100
0010	0100
. . .	
FFFE	0100
FFFF	0000

FIGURE 7

